



Search the Digital Library

Search Articles:

Terms:

all words any words exact phrase subject
 expression stem

In Fields:

<input checked="" type="checkbox"/> Title (60,970)	<input checked="" type="checkbox"/> Reviews (2,602)
<input checked="" type="checkbox"/> Full-Text (53,088)	<input checked="" type="checkbox"/> Index Terms (50,514)
<input checked="" type="checkbox"/> Abstract (16,305)	(Number of articles)

Authors:

all names any name expression soundex

Limit Your Search To:

Publication:**Published Since:**

<input type="text" value="Month"/>	<input type="text" value="Year"/>
<input type="button" value="▼"/>	<input type="button" value="▼"/>

Published Before:

<input type="text" value="Month"/>	<input type="text" value="Year"/>
<input type="button" value="▼"/>	<input type="button" value="▼"/>

[\[Help \]](#)

The Digital Library is published by the Association for Computing Machinery. Copyright © 2000 ACM, Inc.



search results

Page: **1 of 1**

Articles: 1-4 of 4 Ordered By Score

Search: [New](#) | [Undo](#) | [Refine](#)

Order By: [Publication](#) | [Score](#) | [Publication Date](#)

View: [Brief Listing](#) | [Full Listing](#) | [Search Expression](#) | [All Articles](#) |

[+Page Size](#) | [-Page Size](#) | [Help](#)

No.	Article	Score
1)	A testbed for characterizing dynamic response of virtual environment spatial sensors ; Bernard D. Adelstein, Eric R. Johnston and Stephen R. Ellis; <i>Proceedings of the fifth annual ACM symposium on User interface software and technology</i> , 1992, Pages 15 - 22 [Find Related Articles]	8
2)	Ultra-low-power analog associative memory core using flash-EEPROM-based programmable capacitors ; A. Kramer, R. Canegallo, M. Chinosi, D. Doise, G. Gozzini, P. L. Rolandi, M. Sabatini and P. Zabberoni; <i>Proceedings 1995 international symposium on Low power design</i> , 1995, Pages 203 - 208 [Find Related Articles]	8
3)	Text mining: finding nuggets in mountains of textual data ; Jochen Dörre, Peter Gerstl and Roland Seiffert; <i>Proceedings of the fifth ACM SIGKDD international conference on Knowledge discovery and data mining</i> , 1999, Pages 398 - 401 [Find Related Articles]	5
4)	Validation methodologies for complex, hybrid, HWIL, 6DOF missile simulations--a structured approach ; Robert M. Gravitz and William F. Waite; <i>Proceedings of the 1988 conference on Winter simulation</i> , 1988, Pages 775 - 782 [Find Related Articles]	5

go to page: **1 of 1**

The Digital Library is published by the Association for Computing Machinery. Copyright 1999, 2000 ACM, Inc.

[library home](#)

[list alphabetically](#)

[list by SIG](#)

[search library](#)

[register DL](#)

[subscribe DL](#)

[feedback](#)

Enter your query below, and then click *Start Search*

(eras* <near/2> block) and (flash or eeprom or eprom or nonvolatile or non-volatile)

Collections to Search:

Journals Conference Proceedings Standards

Additional Criteria:

Match similar subject terms Latest additions to IEL

Publication Year(s): to

Results List: Sort by in order

List results at a time Display line citation

If you have any question about IEL Online and how it works, check the Frequently Asked Questions or email [IEL Online Support](#).

To let us know what you think about IEL Online, email your comments to [IEEE Electronic Products](#)

| [IEL Online Home](#) | [Search](#) | [Advanced Search](#) | [What's New](#) | [Help](#) | [Logout](#) |
| [FAQ's](#) | [Support](#) | [Comments](#) |

Copyright 1999 Institute of Electrical and Electronics Engineers. All rights reserved.

(eras* <near/2> block) and (flash or eeprom or eprom) [Refine](#)**Collection:** Journals Conferences StandardsYour search matched **11** of **656543** documents.

11 are presented on this page, sorted by Score in descending order.

DOC TYPE [VIEW ISSUE](#) [TOC](#)[VIEW FULL PAGE](#) [VIEW CITATION](#)

CNF

**A microcontroller with 100 K bytes embedded flash EEPROM**

Kuo, C.; Toms, T.; Weidner, M.; Choe, H.; Shum, D.; Ko-Min Chang; Smith, P.

Solid-State and Integrated Circuit Technology, 1995 4th International Conference on, 1995, Page(s): 138 -140

CNF

**Flash memory BIOS for PC and notebook computers**

Jex, J.

Communications, Computers and Signal Processing, 1991., IEEE Pacific Rim Conference on, 1991, Page(s): 692 -695 vol.2

CNF

**Row-redundancy scheme for high-density flash memory**

Mihara, M.; Nakayama, T.; Ohkawa, M.; Kawai, S.; Miyawaki, Y.; Terada, Y.; Ohi, M.; Onoda, H.; Ajika, N.; Hatanaka, M.; Miyoshi, H.; Yoshihara, T.

Solid-State Circuits Conference, 1994. Digest of Technical Papers. 41st ISSCC., 1994 IEEE International, 1994, Page(s): 150 -151

CNF

**A high density NAND EEPROM with block-page programming for microcomputer applications**

Momodomi, M.; Iwata, Y.; Tanaka, T.; Itoh, Y.; Shirota, R.; Masuoka, F.

Custom Integrated Circuits Conference, 1989., Proceedings of the IEEE 1989, 1989, Page(s): 10.1/1 -10.1/4

PER

**A quick intelligent page-programming architecture and a shielded bitline sensing method for 3 V-only NAND flash memory**

Tanaka, T.; Tanaka, Y.; Nakamura, H.; Sakui, K.; Oodaira, H.; Shirota, R.; Ohuchi, K.; Masuoka, F.; Hara, H.

Solid-State Circuits, IEEE Journal of

Volume: 29 11, N v. 1994, Page(s): 1366 -1373

CNF

**A 35 ns-cycle-time 3.3 V-only 32 Mb NAND flash EEPROM**

Imamiya, K.; Iwata, Y.; Sugiura, Y.; Nakamura, H.; Oodaira, H.; Momodomi, M.; Ito, Y.; Watanabe, T.; Araki, H.; Narita, K.; Masuda, K.; Miyamoto, J.

Solid-State Circuits Conference, 1995. Digest of Technical Papers. 41st ISSCC, 1995 IEEE International, 1995,

CNF



A 3.3 V-only 16 Mb DINOR flash memory

Kobayashi, S.; Mihara, M.; Miyawaki, Y.; Ishii, M.; Futatsuya, T.; Hosogane, A.; Ohba, A.; Terada, Y.; Ajika, N.; Kunori, Y.; Yuzuriha, K.; Hatanaka, M.; Miyoshi, H.; Yoshihara, T.; Uji, Y.; Matsuo, A.; Taniguchi, Y.; Kiguchi, Y.

Solid-State Circuits Conference, 1995. Digest of Technical Papers. 41st ISSCC, 1995 IEEE International , 1995 ,
Page(s): 122 -123, 349

CNF



New submicron non-volatile memory modules for 16/32-bit devices

Kuo, C.; Morton, B.; Toms, T.; Weidner, M.; Chrudimsky, D.; Choe, H.; Bowers, M.; Yeon-seuk Kim; Chang, K.M.; Smith, P.

VLSI Technology, Systems, and Applications, 1993.

Proceedings of Technical Papers. 1993 International Symposium on , 1993 , Page(s): 343 -347

CNF



A 1 Mbit EEPROM with MONOS memory cell for semiconductor disk application

Nozaki, T.; Tanaka, T.; Kijiiya, Y.; Kinoshita, E.; Tsuchiya, T.; Hayashi, Y.

VLSI Circuits, 1990. Digest of Technical Papers., 1990 Symposium on , 1990 , Page(s): 101 -102

PER



A 35 ns cycle time & 3 V only 32 Mb NAND flash EEPROM

Iwata, Y.; Imamiya, K.; Sugiura, Y.; Nakamura, H.; Oodaira, H.; Momodomi, M.; Itoh, Y.; Watanabe, T.; Araki, H.; Narita, K.; Masuda, K.; Miyamoto, J.-I.

Solid-State Circuits, IEEE Journal of

Volume: 30 11 , Nov. 1995 , Page(s): 1157 -1164

PER



A 60-ns 16-Mb flash EEPROM with program and erase sequence controller

Nakayama, T.; Kobayashi, S.; Miyawaki, Y.; Terada, Y.; Ajika, N.; Ohi, M.; Arima, H.; Matsukawa, T.; Yoshihara, T.; Suzuki, K.

Solid-State Circuits, IEEE Journal of

Volume: 26 11 , Nov. 1991 , Page(s): 1600 -1605